

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No	10/082,599
Filing Date	February 22, 2002
Inventorship	
Assignee	Micron Technology, Inc.
Group Art Unit	1763
Examiner	Karla A. Moore
Attorney's Docket No	MI22-1941
Customer No.	021567
Title: Interfacial Structure for Semiconductor Subs	trate Processing Chambers
and Substrate Transfer Chambers and for	Semiconductor Substrate
Processing Chambers and Accessory Attach	ments, and Semiconductor
Substrate Processor	

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. Copies of the cited art are attached hereto. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed before the mailing of a first office action after the filing of a Request for Continued Examination. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 4/29/64

Mark S. Matkin, Reg. No. 32,268

## EL979955580

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. MI22-1941

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OTHER RE	FEREN	ICES (i	ncluding Author,	Title, Date, Pe	ertinent Pages, Etc.)								
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considered. Include copy of this form with next communication to applicant.